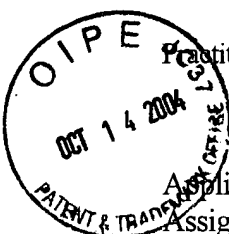


IFW



Patitioner's Docket No.: MIC-12693-1P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shekar Mallikarjunaswamy
Assignee: Micrel, Incorporated
Serial No.: 10/665,806 Art Unit.: 2811
Filed: 09/19/2003 Examiner: Douglas W. Owens
For: "Thick Gate Oxide Transistor And Electrostatic Discharge Protection Utilizing Thick Gate Oxide Transistors"

October 8, 2004

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the outstanding Office Action dated 09/27/2004, in which the Examiner imposed a restriction requirement to election of invention for the above-referenced application, Applicant elects to prosecute the invention as in Group 1, Claims 1-20 drawn to a protection circuit, without traverse.

Applicant reserves the right to file divisional applications on the non-elected claims.

Respectfully submitted,

Patrick T. Bever,
Reg. No. 33,834
Attorney for Applicant(s)

Customer No.: 022888
Tel. No.: (408) 451-5902
Fax No.: (408) 451-5908

I hereby certify that this correspondence is being deposited with the United States Postal Service as FIRST CLASS MAIL in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

10/8/2004
Date

Signature: Rebecca A. Baumann